

FIG. 1
cell of a prior art DAC

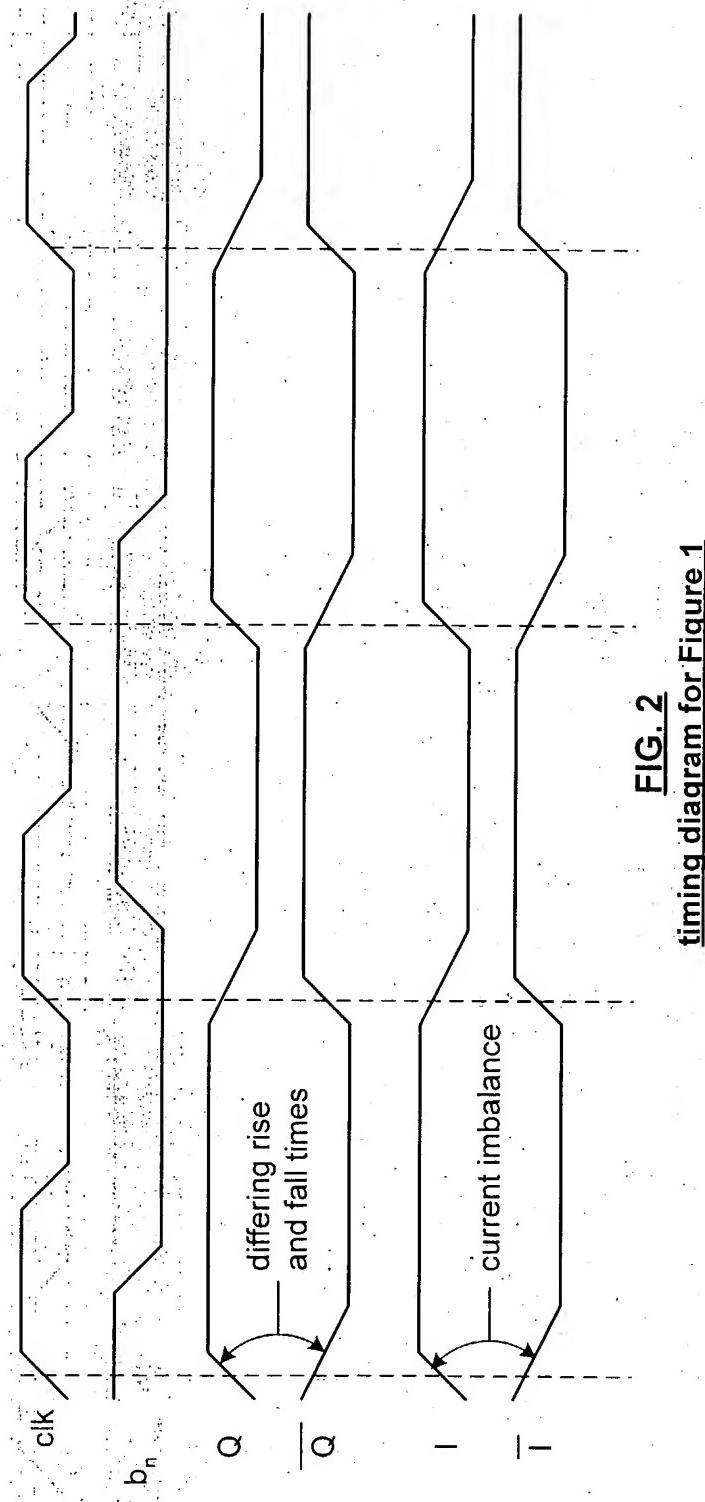
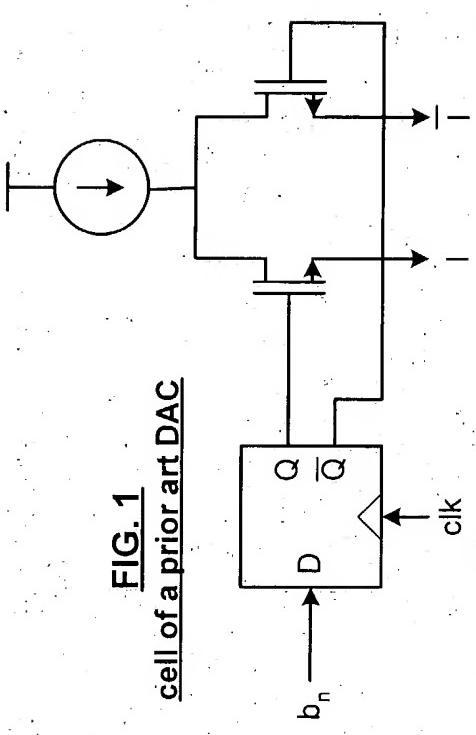


FIG. 2
timing diagram for Figure 1

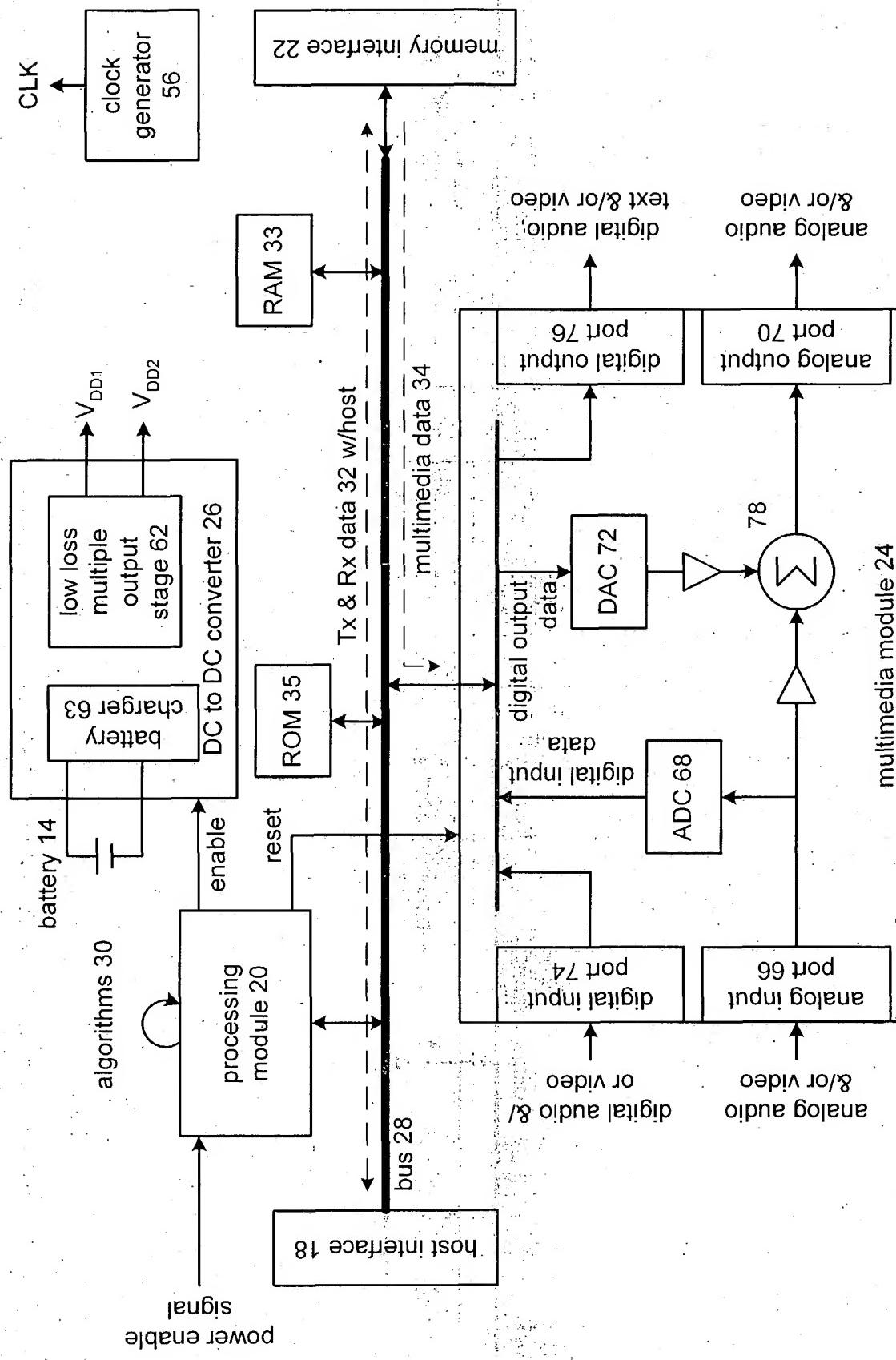


FIG. 3
integrated circuit 12

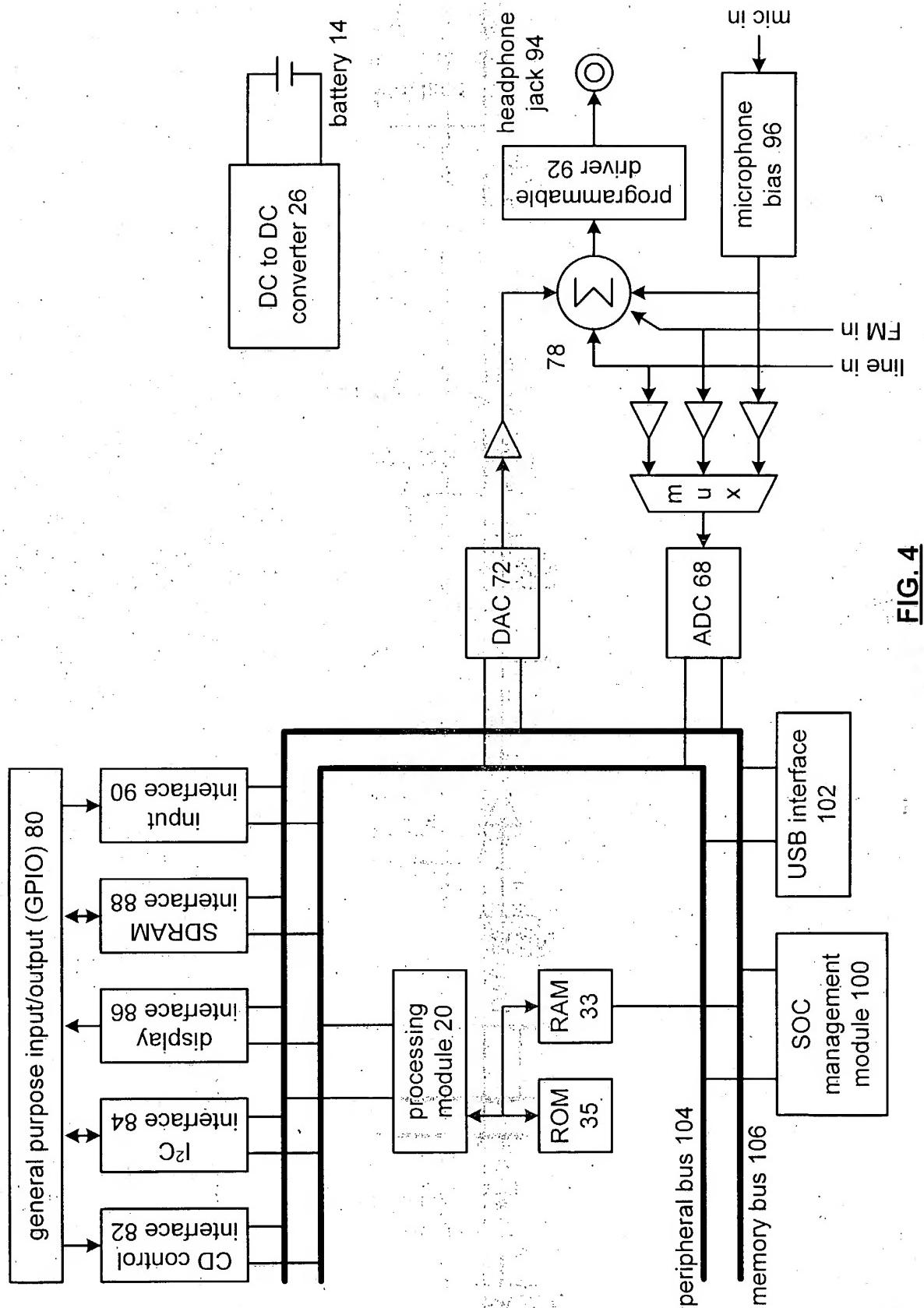


FIG. 4
Integrated circuit 12-1

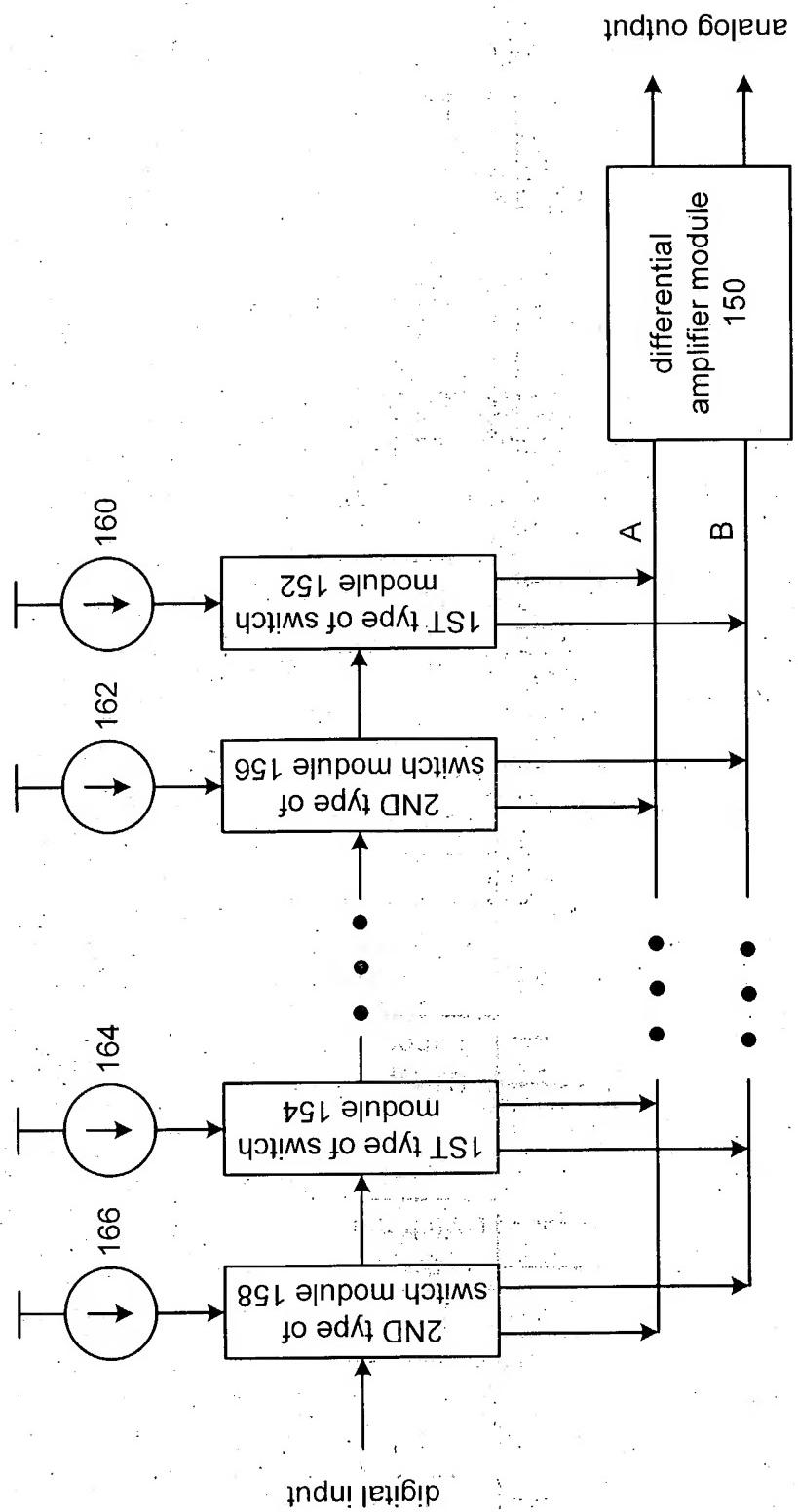


FIG. 5
DAC 72

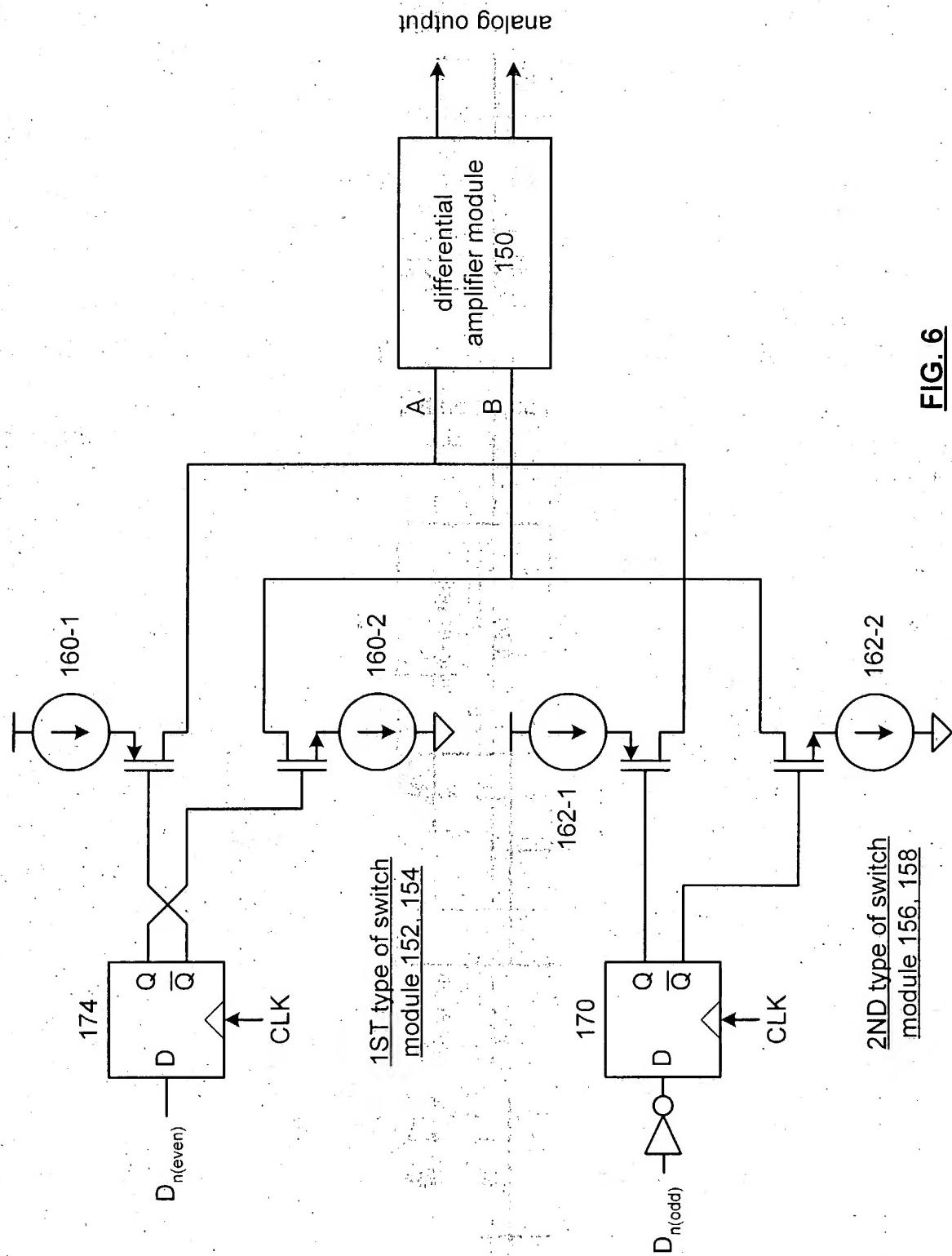


FIG. 6

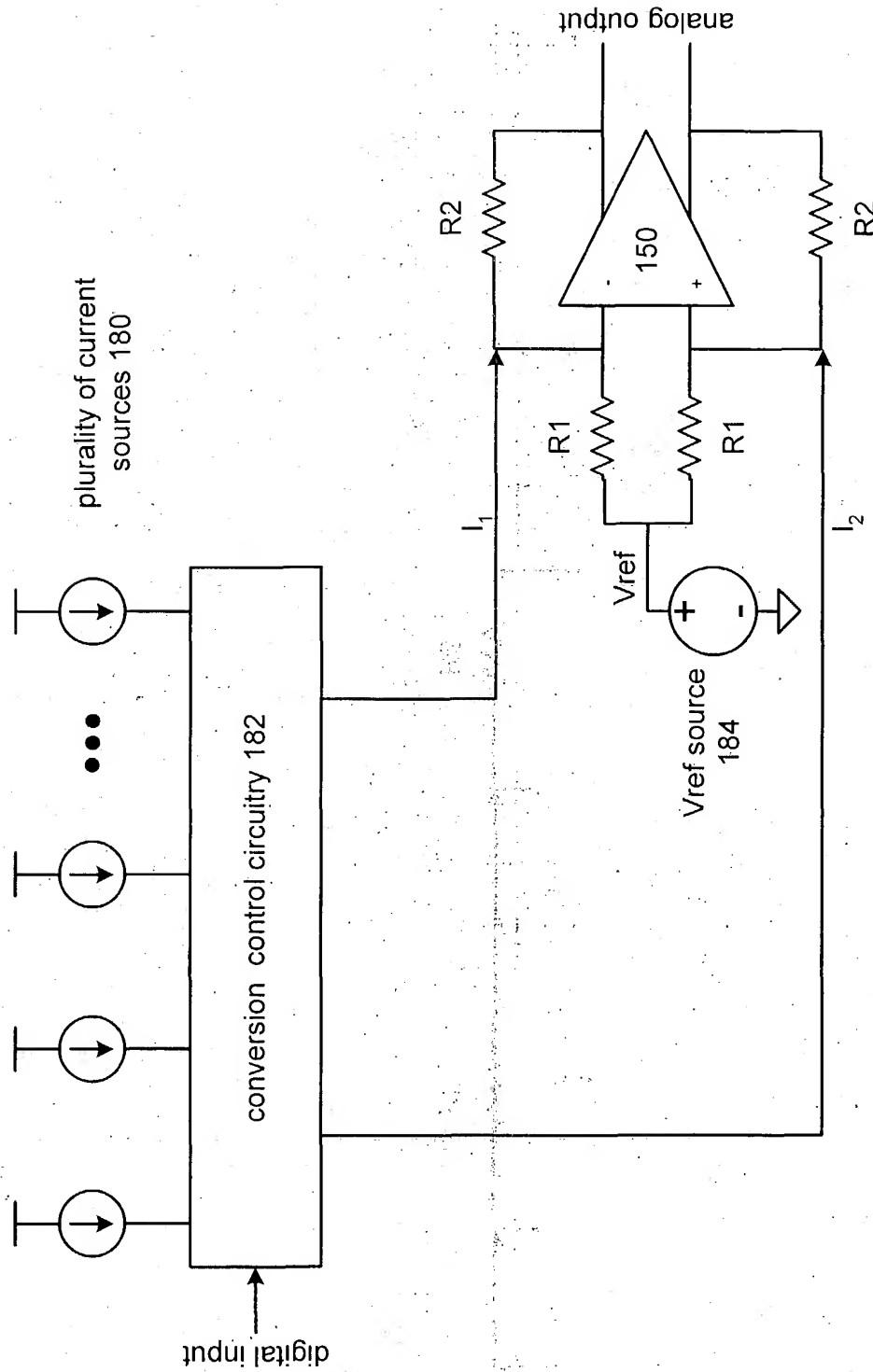


FIG. 7
DAC 72

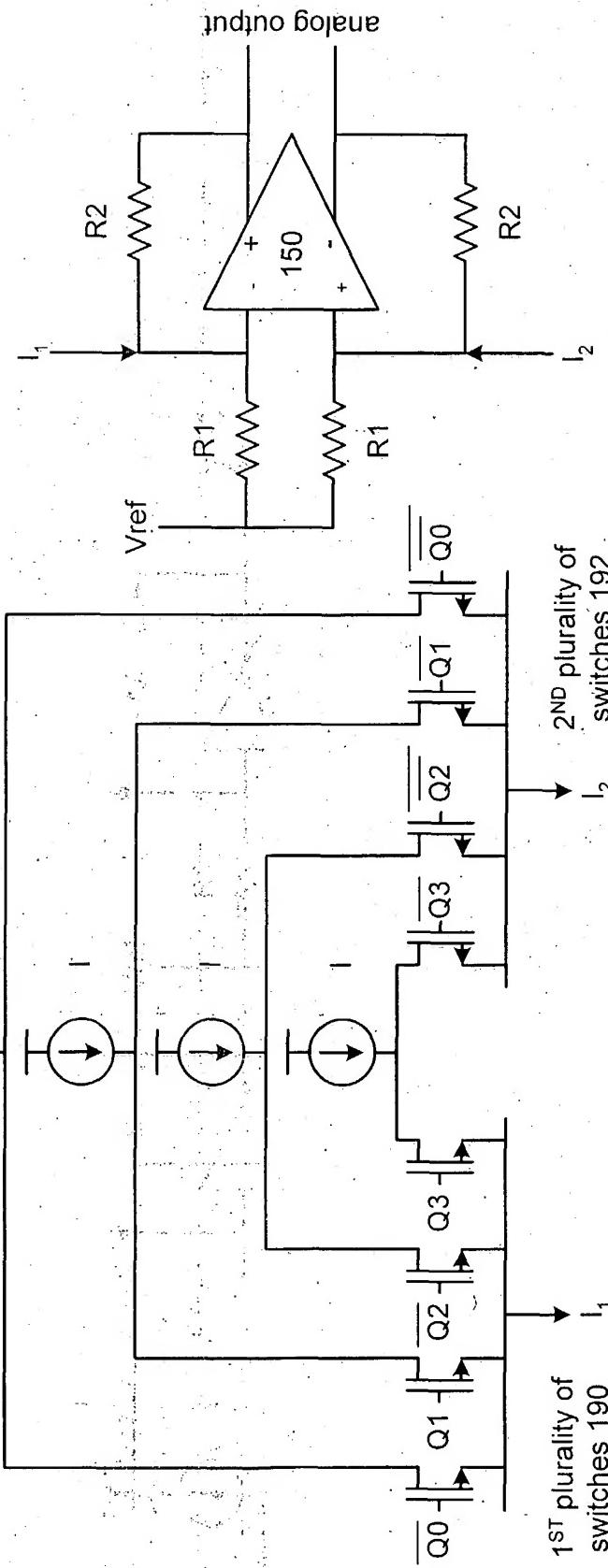
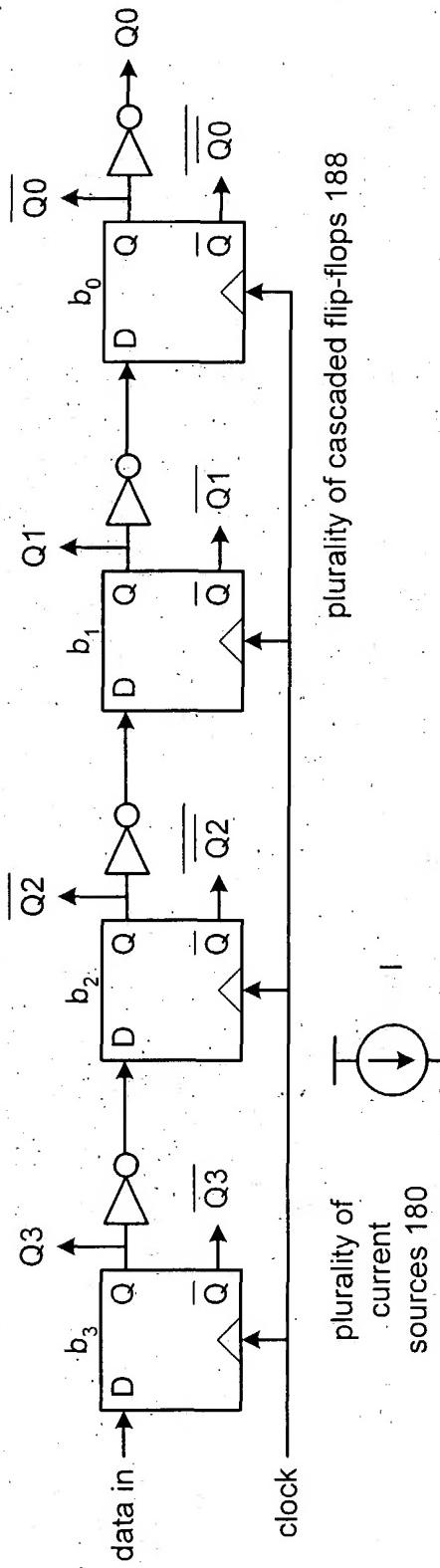


FIG. 8
DAC 72

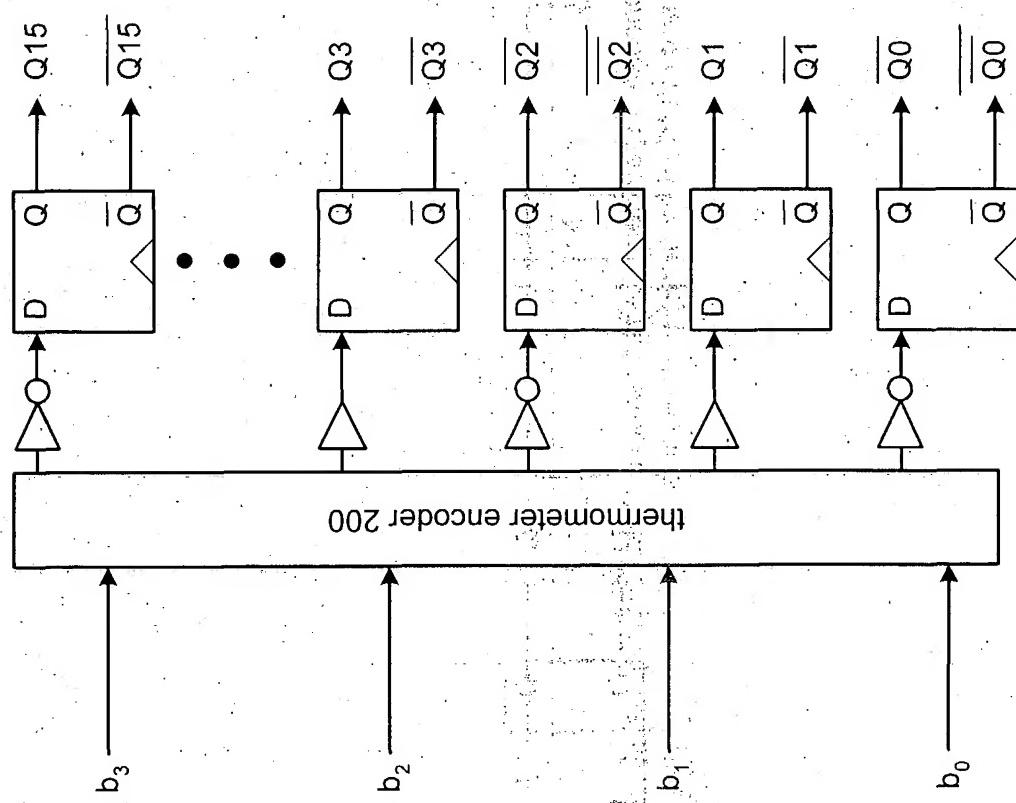
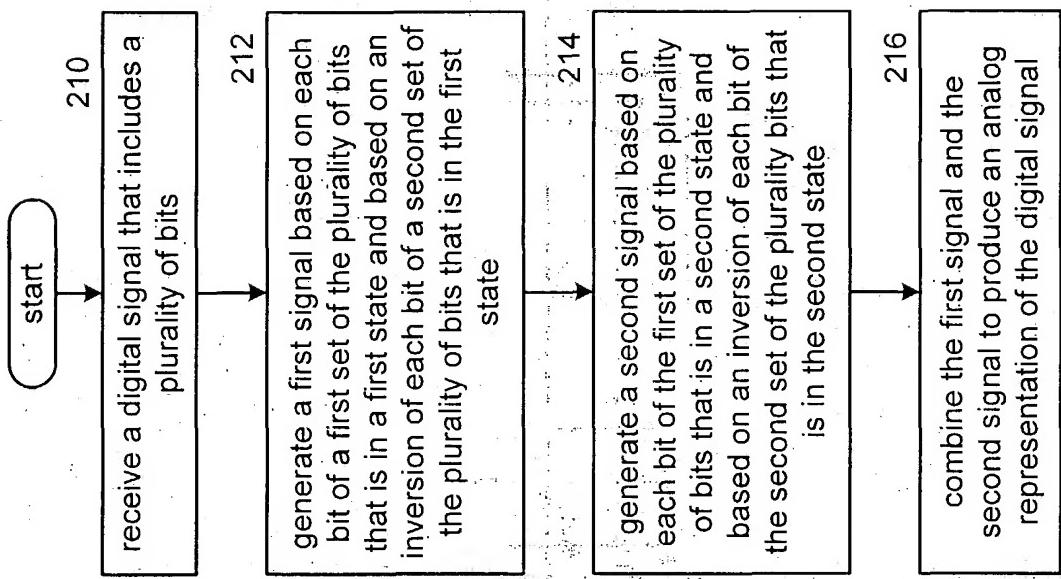


FIG. 9
portion of conversion
control circuit 182

FIG. 10